

<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		09/774,888	KOYAMA ET AL.	
Examiner		Art Unit		Page 1 of 1
Howard Weiss		2814		

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
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**FOREIGN PATENT DOCUMENTS**

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	N					
	O					
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	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, S., et al., Silicon Processing for the VLSI Era, Vol. 1: Process Technology, Lattice Press, 1986, p. 343-4
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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